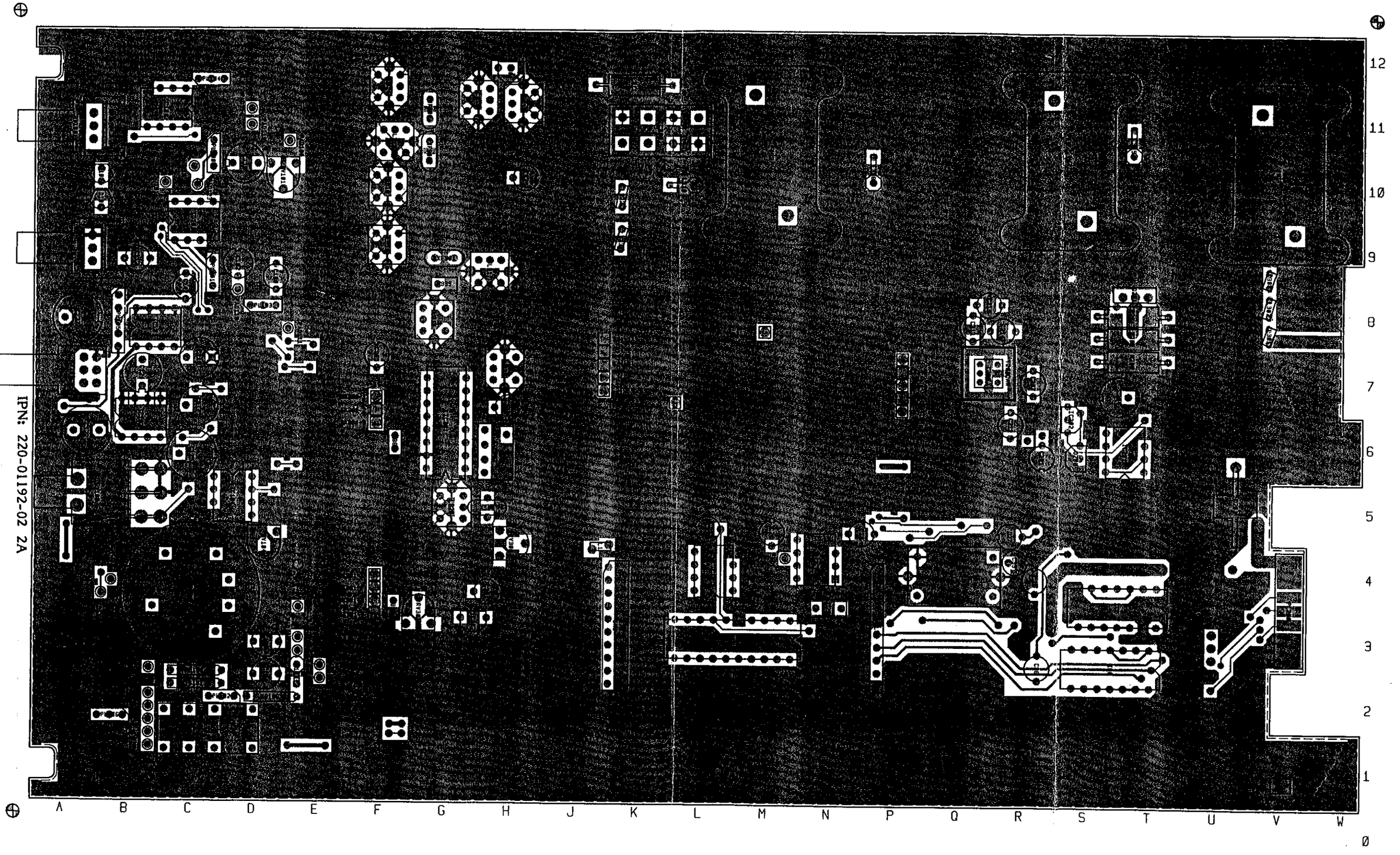



DIAGRAM 5 : T885 PCB LAYOUT - TOP SIDE.

DIAGRAM 5
T885 PCB LAYOUT - TOP SIDE

IPN: 220-01192-02 2A

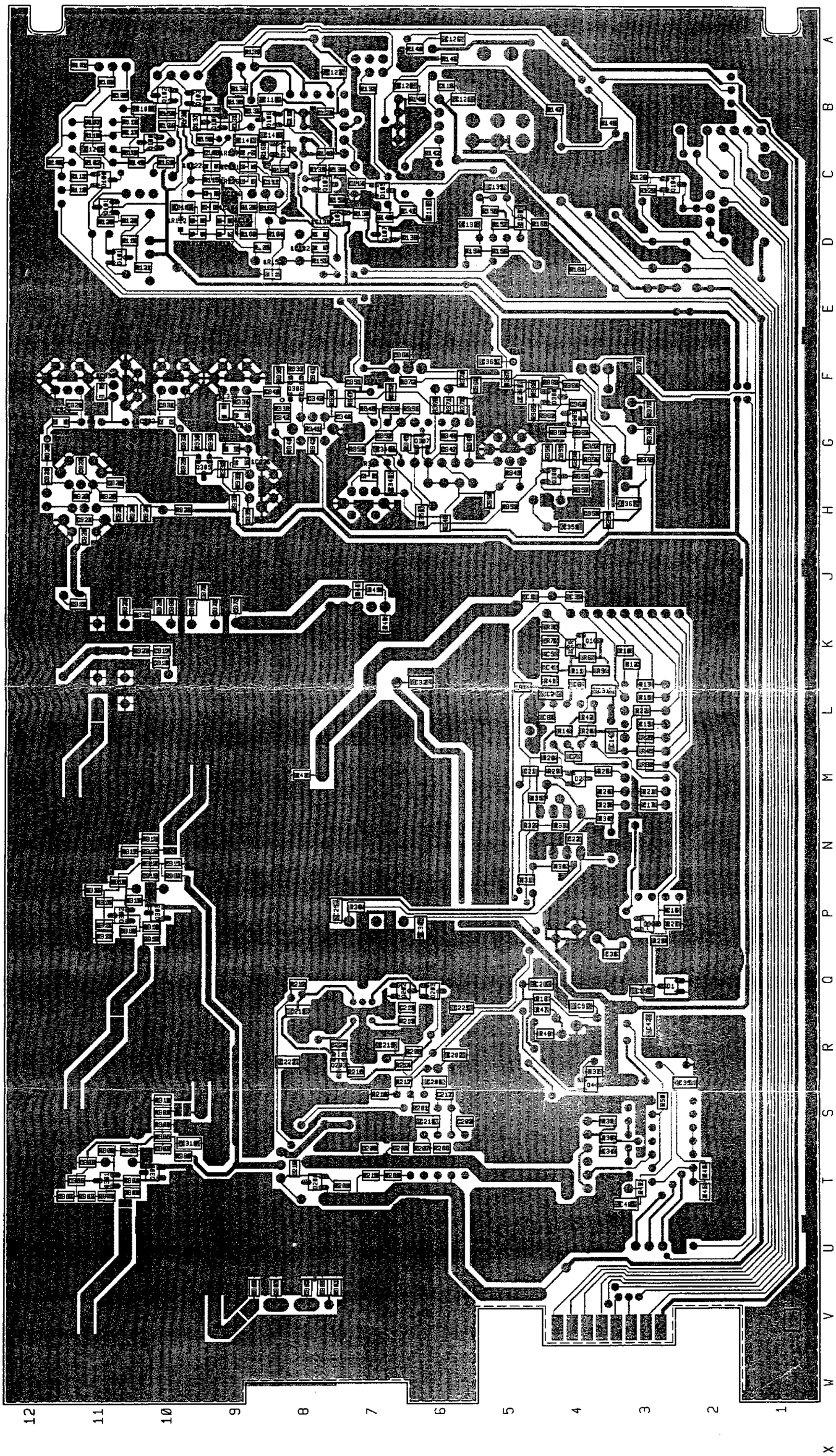
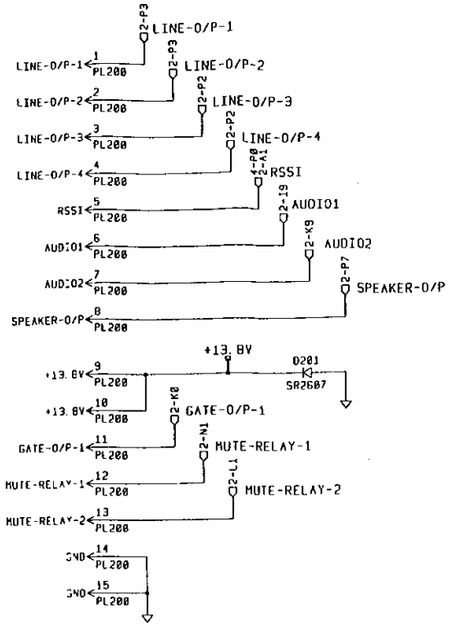
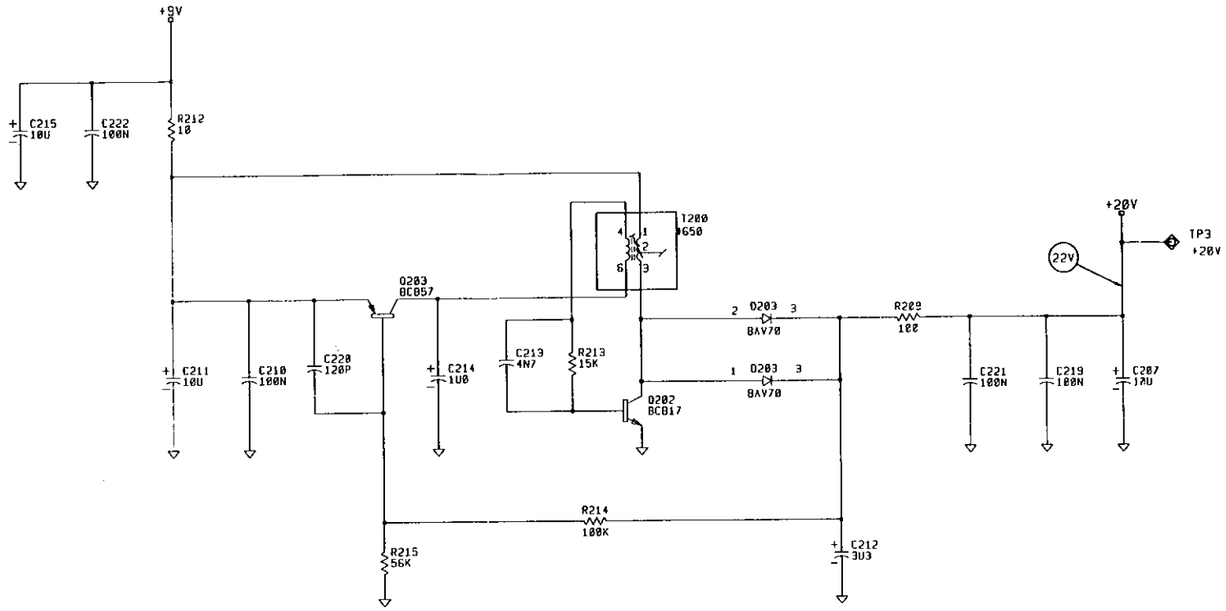
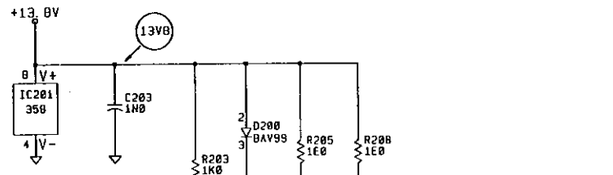


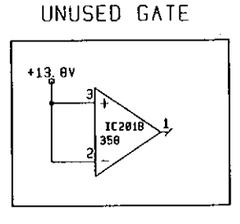
DIAGRAM 6: T885 PCB LAYOUT - BOTTOM SIDE.

DIAGRAM 6
T885 PCB LAYOUT - BOTTOM SIDE




DC CONDITIONS (VOLTS)

| ISSUE: | AMENDMENTS: | DRAWN: | CHKD: | APVD: | DATE: |
|---|---|---|---|---|---|
| 2B | CH/N: 91/02-93 | MC | | | |
| 2A | CH/N: 90/11-557 | WD | | | |
| 1A | | WD | | | |
| A | | WD | | | |
| P1 | | WD | | | |

TITLE

# T885 REGULATOR

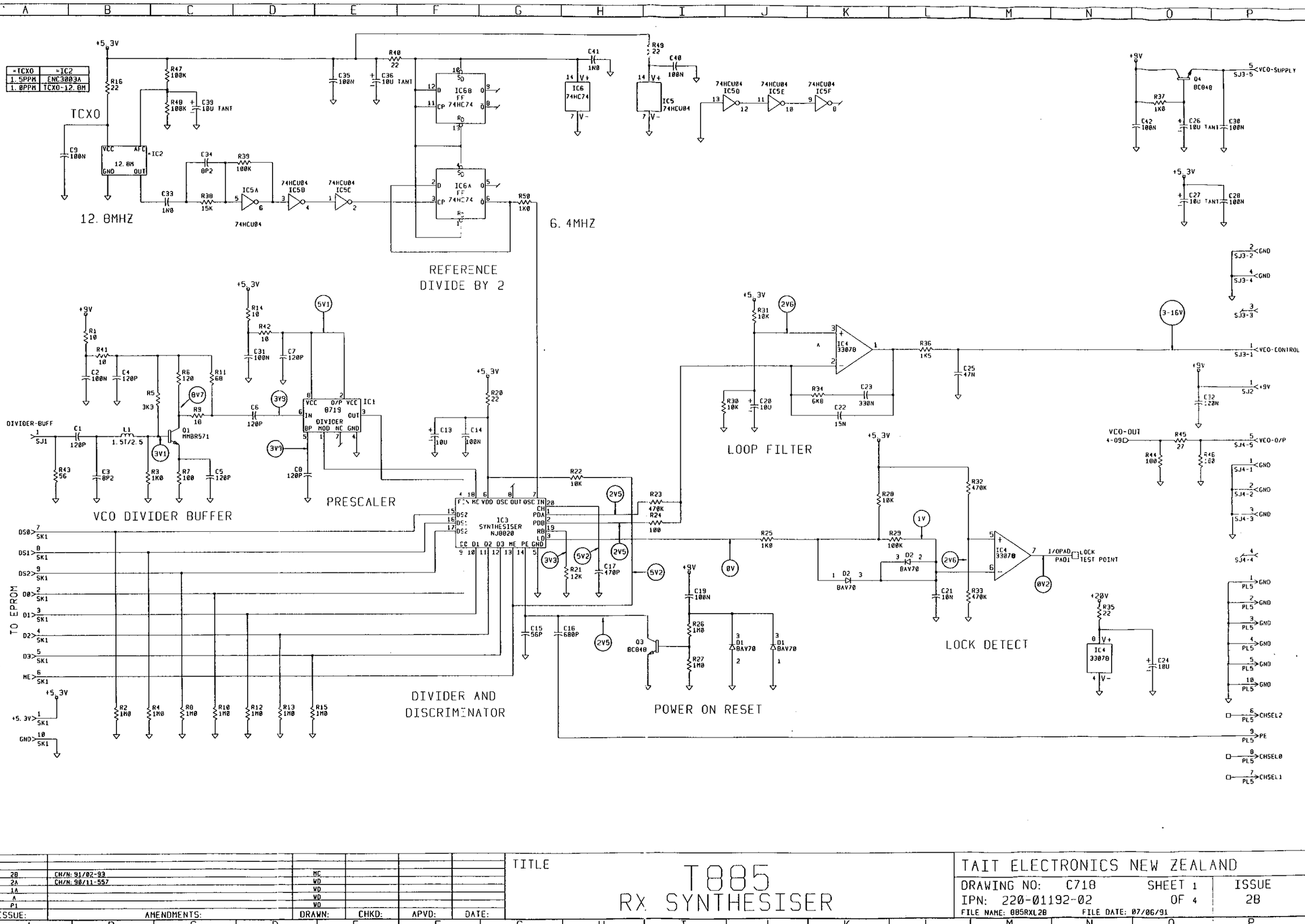
TAIT ELECTRONICS NEW ZEALAND

DRAWING NO: C718 SHEET 3 OF 4 ISSUE 2B

IPN: 220-01192-02

FILE NAME: B05RXL2B FILE DATE: 07/06/91

DIAGRAM 13 REGULATOR CIRCUIT DIAGRAM




| | | | | | | |
|---|---|---|---|---|---|---|
| 2B | CH/N: 91/02-93 | MC | | | | |
| 2A | CH/N: 98/11-557 | WD | | | | |
| 1A | | WD | | | | |
| A | | WD | | | | |
| P1 | | WD | | | | |
| ISSUE: | AMENDMENTS: | DRAWN: | CHKD: | APVD: | DATE: | |

TITLE

# T885
## RX SYNTHESISER

TAIT ELECTRONICS NEW ZEALAND

DRAWING NO: C718 SHEET 1 OF 4 ISSUE 2B

IPN: 220-01192-02

FILE NAME: 885RXL2B FILE DATE: 07/06/91

DIAGRAM 14 SYNTHESISER CIRCUIT DIAGRAM

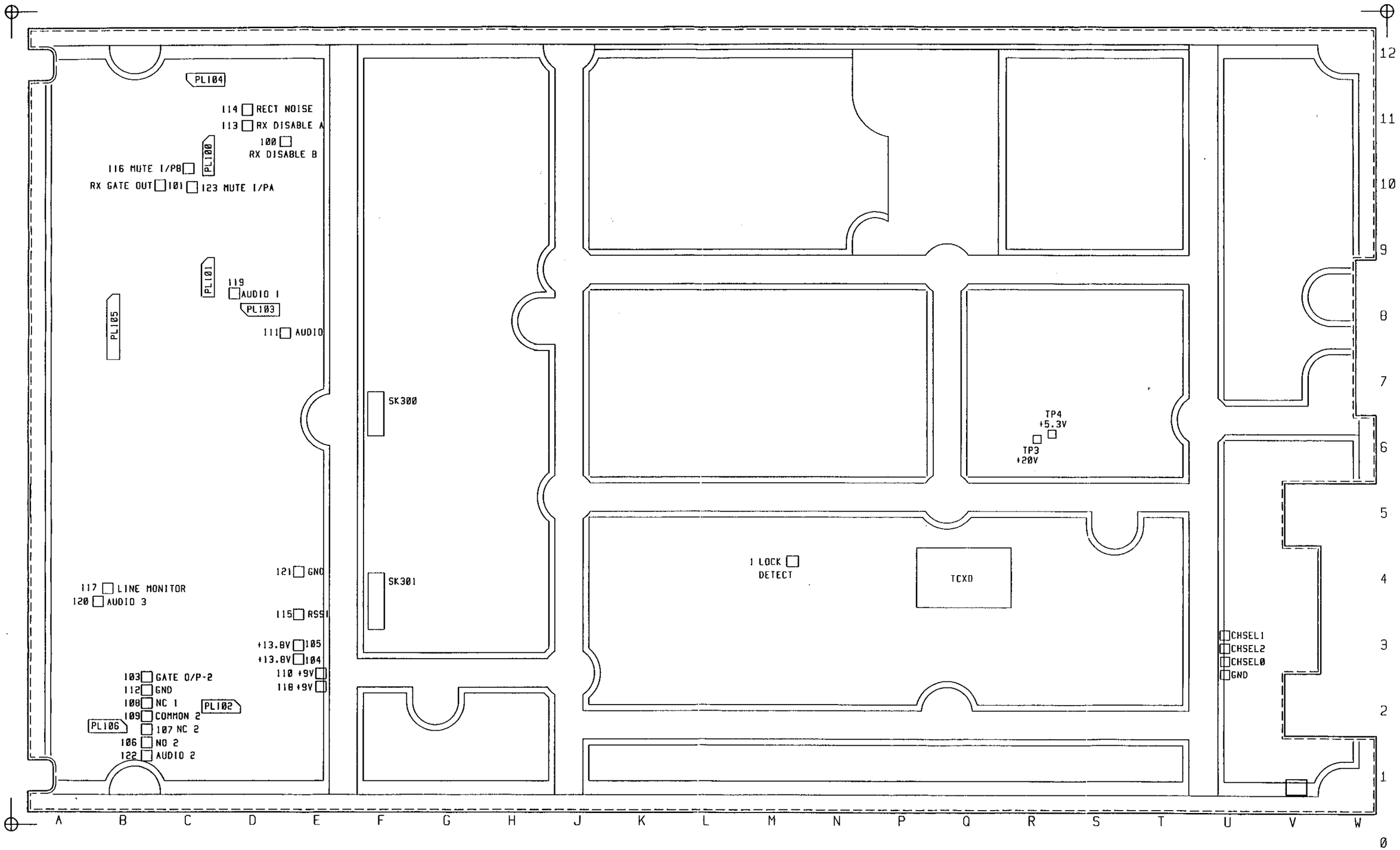



DIAGRAM 7 - T885 TESTPOINTS AND OPTIONS - TOP SIDE.

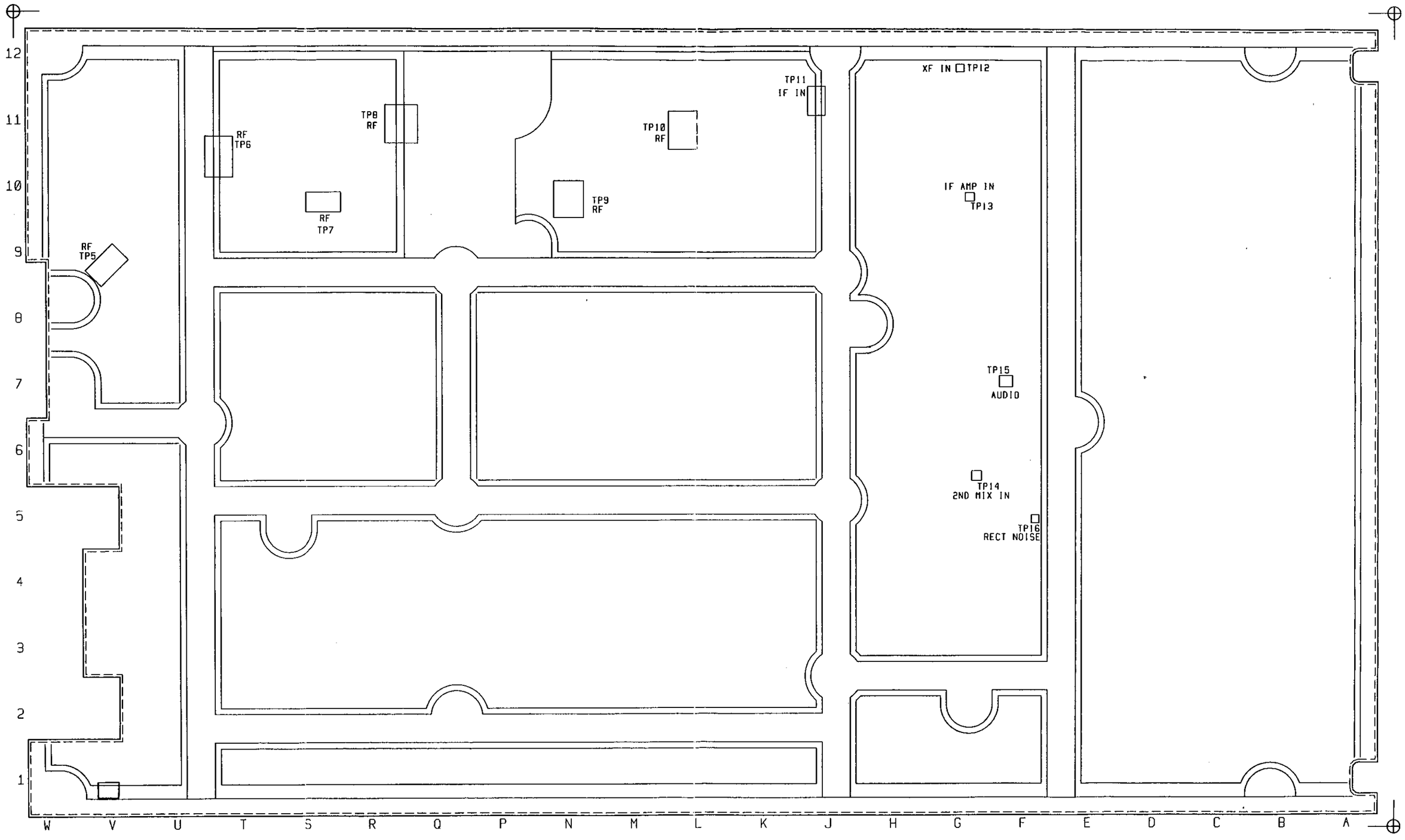
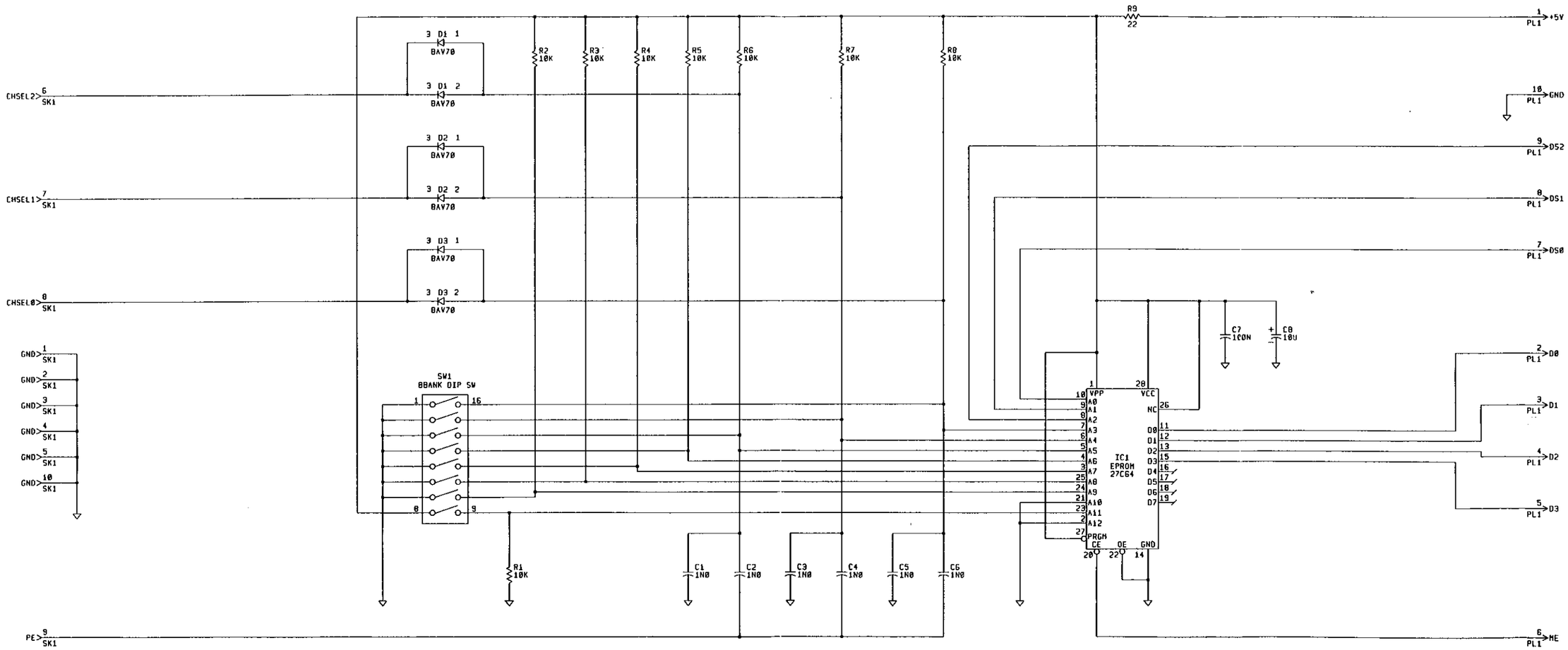



DIAGRAM 8 T885 TEST POINTS AND OPTIONS - BOTTOM SIDE.




| | | | | | | |
|---|---|---|---|---|---|---|
| B | CH. NO. 89/05 257 | B.P | | | 2/6/89 | |
| A | | RH. | | | | |
| PROTI | | RH | | | | |
| ISSUE: | AMENDMENTS: | DRAWN: | CHKD: | APVD: | DATE: | |

TITLE

EPROM

TAIT ELECTRONICS NEW ZEALAND

DRAWING NO: C656 SHEET 1

IPN: 220-00011-44 OF 1

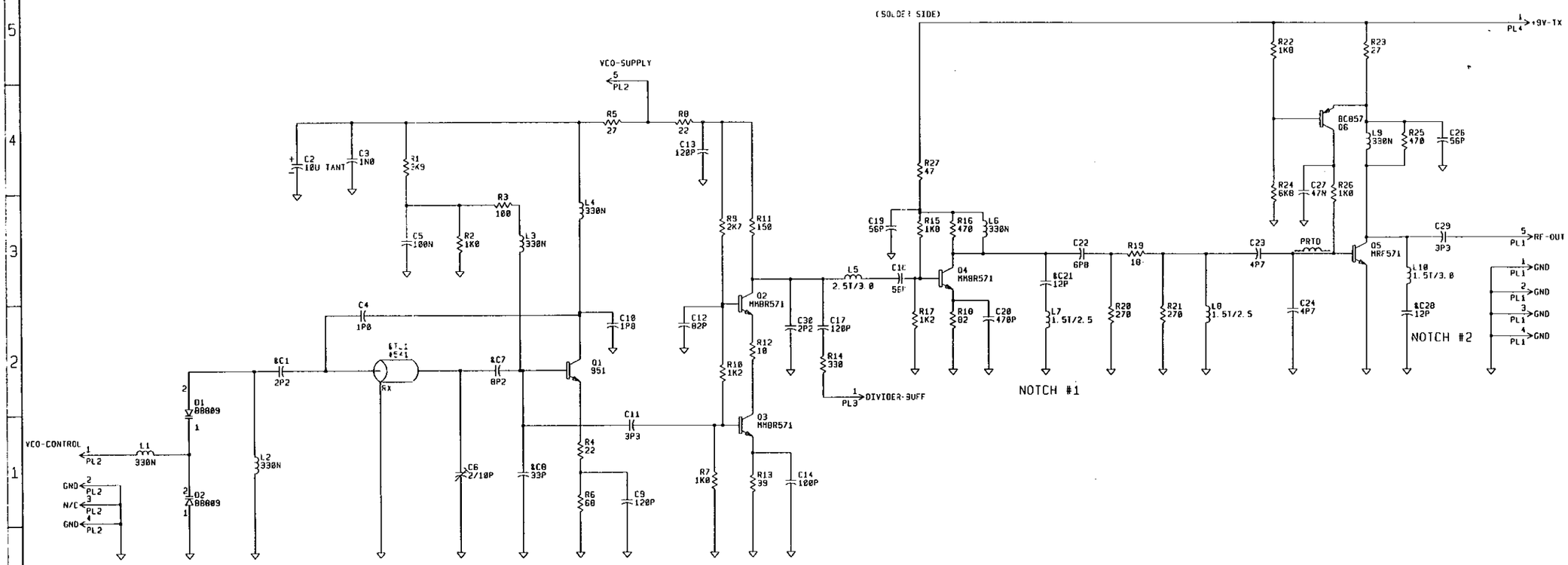
FILE NAME: 855EPLB FILE DATE: 02/06/89

ISSUE B

EPROM CIRCUIT DIAGRAM

# LAYOUT FOR RX LOW

| &VARIANT | | OUTPUT FREQ. MHZ | &TL1 | &TL1 LENGTH | COMPONENTS | | | | |
|---|---|---|---|---|---|---|---|---|---|
| | | | | | &C1 | &C7 | &C8 | &C21 | &C28 |
| 51 | RX | 755-835 | 051-00085-41 | 130MM | 2P2S | 8P2S | 33PS | 12PS | 12PS |
| 52 | RX/TX | 808-915 | 051-00085-61 | 125MM | 1P8S | 4P7S | 27PS | 10PS | 10PS |
| 54 | TX | 850-960 | 051-00085-42 | 120MM | 1P8S | 4P7S | 27PS | 10PS | 8P2S |




VCO — BUFFER — DOUBLER — ATTENUATION PAD — AMPLIFIER

| ISSUE: | AMENDMENTS: | DRAWN: | CHKD: | APVD: | DATE: |
|---|---|---|---|---|---|
| A | UPDATE FROM T99 | WD | | | |
| T99 | UPDATE FROM P2 | WD | | | |
| PROTO2 | UPDATE FROM PROTO1 | BP | | | |
| PROT1 | ORIGINAL | BP | | | |

TITLE

# T88X VCO

TAIT ELECTRONICS NEW ZEALAND

DRAWING NO: C712 SHEET 1 OF 1

IPN: 220-01184-01

FILE NAME: 880VCLA FILE DATE: 14/6/90

ISSUE A

VCO CIRCUIT DIAGRAM

| &IF_WIDTH | &C101 | &C103 | &C104 | &C106 | &C130 | &C131 | &C132 | &R107 | &R109 | &R112 | &R122 | &R126 | &R127 | &R152 |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| 7.5KHZ | 4N7S | 47NT | 2P2S | 47PS | 4N7S | 1N0S | 10NS | 18KS | 15KS | 39KS | 4K7S | 47KS | 100KS | 3K3S |
| 15KHZ | 4N7S | 47NT | 2P2S | 47PS | 10NS | 1N0S | 4N7S | 22KS | 15KS | 39KS | 8K2S | 47KS | 100KS | 4K7S |
| 30KHZ | - | 470PS | - | 220PS | - | 2N2S | 4N7S | 22KS | 0S | 22KS | 3K3S | 27KS | 330KS | 4K7S |

| CTCSS OPTION | PINS TO CONNECT | |
|---|---|---|
| | PL105 | PL106 |
| STD. NON CTCSS | 2-3 | 2-3 |
| CTCSS+SPEECH TO LINE OUTPUT | 1-2 | 2-3 |
| INTERNAL CTCSS | 4-5 | 2-3 |
| EXTERNAL CTCSS | 4-5 | 1-2 |

| ISSUE: | AMENDMENTS: | DRAWN: | CHKD: | APVD: | DATE: |
|---|---|---|---|---|---|
| 2B | CH/N: 91/02-93 | NC | | | |
| 2A | CH/N: 90/11-557 | WD | | | |
| 1A | | WD | | | |
| A | | WD | | | |
| P1 | | WD | | | |

TITLE

# T885 AUDIO PROCESSOR

TAIT ELECTRONICS NEW ZEALAND

DRAWING NO: C718 SHEET 2 OF 4 ISSUE 2B

IPN: 220-01192-02

FILE NAME: 885RXL2B FILE DATE: 07/06/91

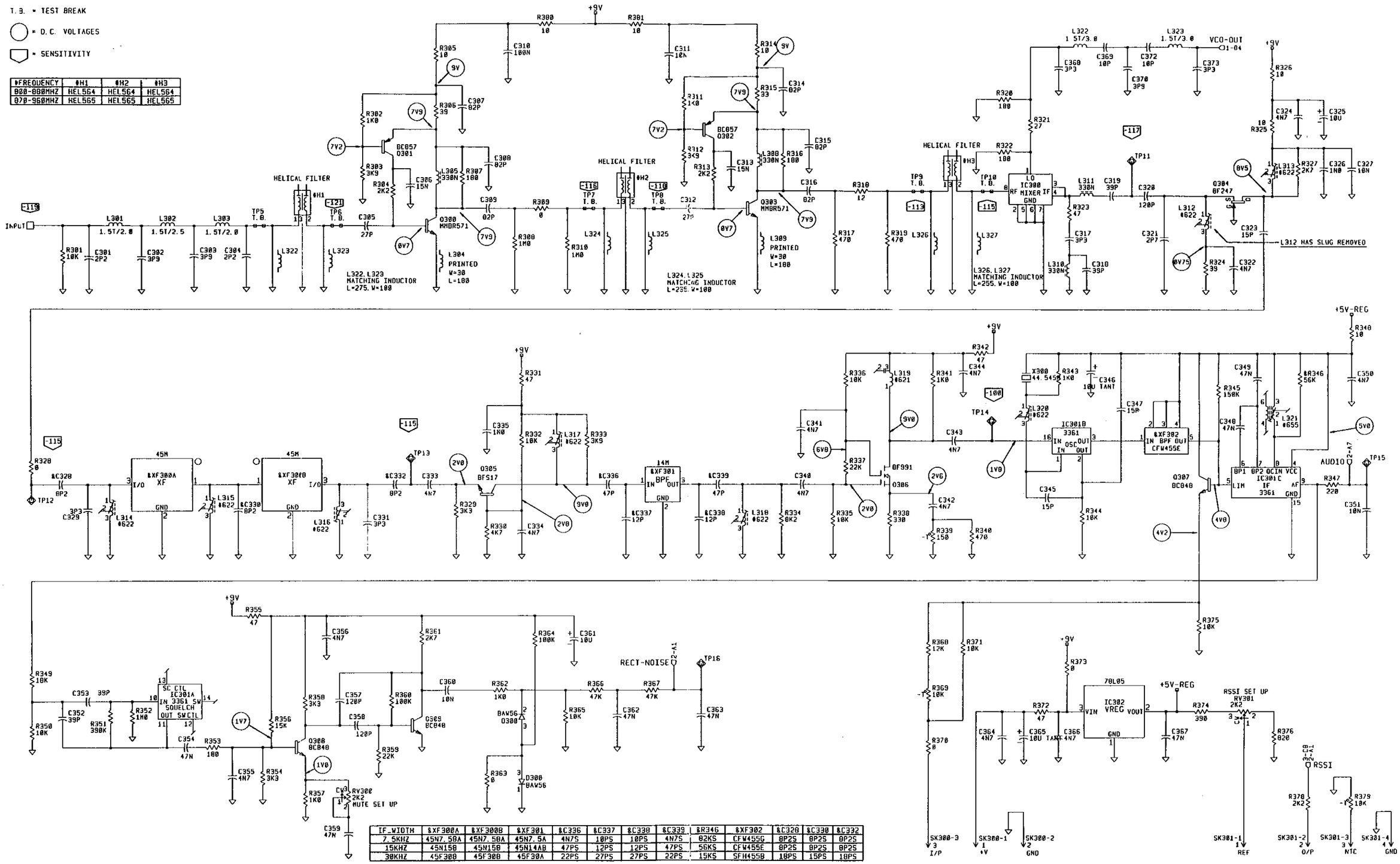
DIAGRAM 11 AUDIO PROCESSOR CIRCUIT DIAGRAM

T.B. = TEST BREAK

= D.C. VOLTAGES

= SENSITIVITY

| *FREQUENCY | @H1 | @H2 | @H3 |
|---|---|---|---|
| 000-880MHZ | HEL564 | HEL564 | HEL564 |
| 870-960MHZ | HEL565 | HEL565 | HEL565 |




| IF_WIDTH | @XF300A | @XF300B | @XF301 | @C336 | @C337 | @C338 | @C339 | @R346 | @XF302 | @C320 | @C330 | @C332 |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| 7.5KHZ | 45N7.5BA | 45N7.5BA | 45N7.5A | 4N7S | 10PS | 10PS | 4N7S | 82KS | CFW455G | 8P2S | 8P2S | 8P2S |
| 15KHZ | 45N15B | 45N15B | 45N14AB | 47PS | 12PS | 12PS | 47PS | 56KS | CFW455E | 8P2S | 8P2S | 8P2S |
| 30KHZ | 45F30B | 45F30B | 45F30A | 22PS | 27PS | 27PS | 22PS | 15KS | SFH455B | 18PS | 15PS | 18PS |

| ISSUE | AMENDMENTS | DRAWN | CHKD | APVD | DATE |
|---|---|---|---|---|---|
| 2B | CH/N: 91/02-93 | MC | | | |
| 2A | CH/N: 90/11-557 | WD | | | |
| 1A | | WD | | | |
| A | | WD | | | |
| F: | | WD | | | |

TITLE

# T885 RECEIVER

TAIT ELECTRONICS NEW ZEALAND

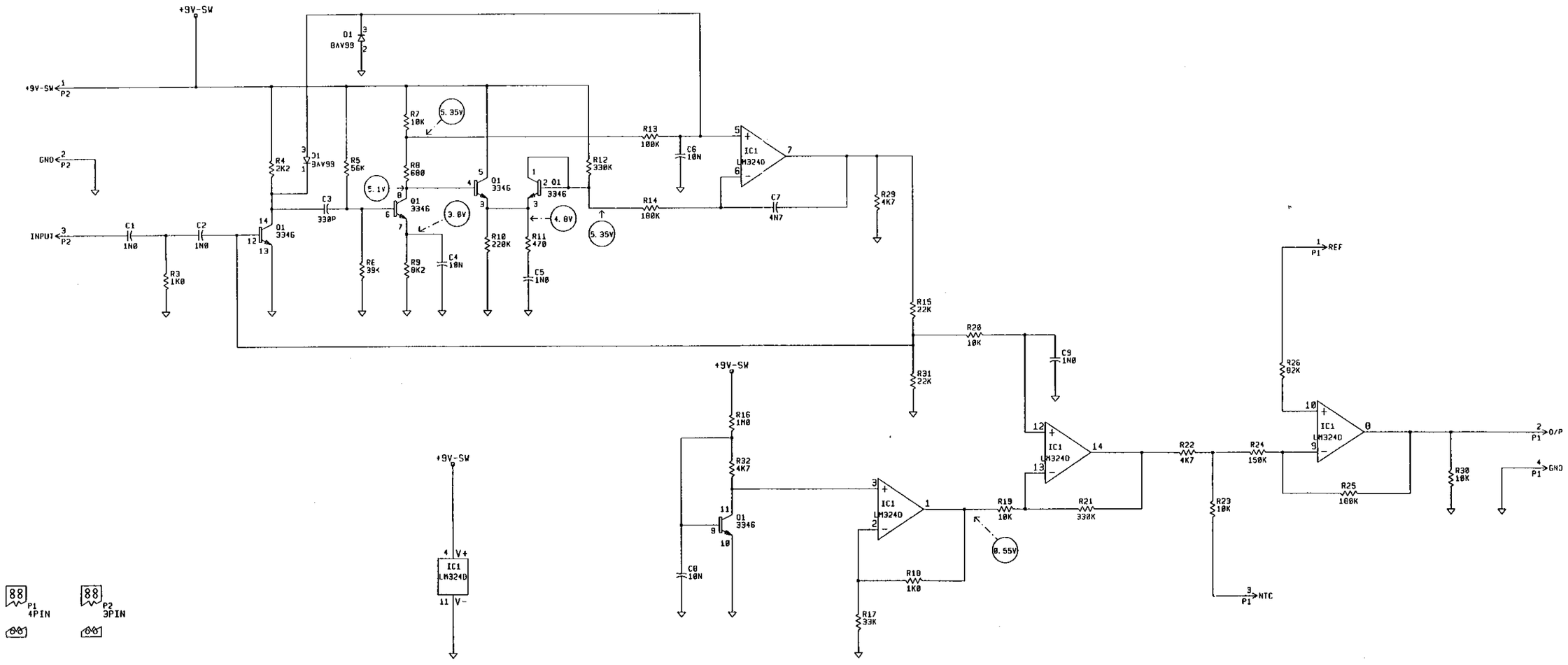
DRAWING NO: C718 SHEET 4 OF 4

IPN: 220-01192-02

ISSUE 2B

FILE NAME: 885RXL2B FILE DATE: 07/06/91

DIAGRAM 12 RECEIVER CIRCUIT DIAGRAM




| ISSUE | AMENDMENTS: | DRAWN: | CHKD: | APVD: | DATE: |
|---|---|---|---|---|---|
| B | VOLTAGE OVERLAY | VD | | | |
| A | ADDED ASSEMBLY DRAWING | GWLK | | | |
| 1-99 | UPDATED SYMBOLS | DE | | | |
| | ORIGINAL | J.S | | | |

TITLE

# T700 RSSI
## SCHEMATIC AND ASSY DRG

TAIT ELECTRONICS NEW ZEALAND

DRAWING NO: C536 SHEET 1
IPN: 220-00011-38 OF 1
FILE NAME: 700RSLB FILE DATE: 10/10/09

ISSUE B

DIAGRAM 16 RSSI CIRCUIT DIAGRAM